

Rajasthan Technical University, Kota
Teaching & Scheme of Examination for B.Tech. (Computer Engineering)
Session 2009-10 (Main Examination)

V Sem.

Sub. Code	Name of Subject	Teaching Periods			Duration of Exams (Hours)	Maximum Marks Allocation				
		L	T	P		Internal	End Term	Sessional	Practical	Total
5CS1	Software Engineering	3	1	-	3	20	80	-	-	100
5CS2	Computer Architecture	3	-	-	3	20	80	-	-	100
5CS3	Database Management Systems	3	-	-	3	20	80	-	-	100
5CS4	Computer Graphics	3	-	-	3	20	80	-	-	100
5CS5	Telecommunication Fundamentals	3	1	-	3	20	80	-	-	100
5CS6.1	Logic & Functional Programming	3	-	-	3	20	80	-	-	100
5CS6.2	Information Theory and Coding									
5CS6.3	Advanced Data Structure									
Total		18	2	-	-	120	480	-	-	600
5CS7	Software Engineering Lab	-	-	2+2	3	-	-	60	40	100
5CS8	Computer Architecture Lab	-	-	2+2	3	-	-	60	40	100
5CS9	Database Management Lab	-	-	2	3	-	-	45	30	75
5CS10	Computer Graphics Lab	-	-	2	3	-	-	45	30	75
Discipline & Extra Curricular Activities		-	-	-	-	-	-	-	-	50
Total		-	-	12	-	-	-	210	140	400
Grand Total		18	2	12	-	120	480	210	140	1,000

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Sub. Code	Name of Subject	Teaching Periods			Duration of Exams (Hours)	Maximum Marks Allocation				
		L	T	P		Internal	End Term	Sessional	Practical	Total
6CS1	Operating Systems	3	1	-	3	20	80	-	-	100
6CS2	Computer Networks	3	-	-	3	20	80	-	-	100
6CS3	Design & Analysis of Algorithms	3	-	-	3	20	80	-	-	100
6CS4	Embedded Systems	3	1	-	3	20	80	-	-	100
6CS5	Theory Of Computation	3	-	-	3	20	80	-	-	100
6CS6.1	Digital Signal Processing	3	-	-	3	20	80	-	-	100
6CS6.2	Advanced Software Engineering									
6CS6.3	Microwave and Satellite Communication									
Total		18	2	-	-	120	480	-	-	600
6CS7	Shell Programming Lab	-	-	2	3	-	-	45	30	75
6CS8	Network lab	-	-	2	3	-	-	60	40	100
6CS9	Web Programming lab	-	-	2+2	3	-	-	45	30	75
6CS10	Microcontroller lab	-	-	2+2	3	-	-	60	40	100
Discipline & Extra Curricular Activities		-	-	-	-	-	-	-	-	50
Total		-	-	12	-	-	-	210	140	400
Grand Total		18	2	12	-	120	480	210	140	1,000

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VII Sem.

Sub. Code	Name of Subject	Teaching Periods			Duration of Exams (Hours)	Maximum Marks Allocation				
		L	T	P		Internal	End Term	Sessional	Practical	Total
7CS1	Compiler Construction	3	-	-	3	20	80	-	-	100
7CS2	Data Mining And Ware Housing	3	1	-	3	20	80	-	-	100
7CS3	Logic Synthesis	3	-	-	3	20	80	-	-	100
7CS4	Artificial Intelligence	3	-	-	3	20	80	-	-	100
7CS5	Multimedia Systems	3	-	-	3	20	80	-	-	100
7CS6.1	Service Oriented Architectures	3	-	-	3	20	80	-	-	100
7CS6.2	Optical Communication									
7CS6.3	Real Time Systems									
Total		18		-	-	120	480	-	-	600
7CS7	Compiler Design Lab	-	-	2+2	3	-	-	45	30	75
7CS8	Data Mining And Ware Housing Lab	-	-	2+2	3	-	-	45	30	75
7CS9	Logic Synthesis Lab	-	-	2	3	-	-	30	20	50
7CS10	Project Stage I	-	-	2/2	-	-	-	30	20	50
7CS11	Practical Training Seminar	-	-	2	-	-	-	40+20	40	100
Discipline & Extra Curricular Activities		-	-	-	-	-	-	-	-	50
Total		-	-	13	-	-	-	210	140	400
Grand Total		18	1	13	-	120	480	210	140	1,000

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VIII Sem.

Sub. Code	Name of Subject	Teaching Periods			Duration of Exams (Hours)	Maximum Marks Allocation				
		L	T	P		Internal	End Term	Sessional	Practical	Total
8CS1	Information System and Securities	3	-	-	3	20	80	-	-	100
8CS2	CAD FOR VLSI Design	3	1	-	3	20	80	-	-	100
8CS3	Advanced computer Architectures	3	1	-	3	20	80	-	-	100
8CS4.1	Distributed Systems	3	-	-	3	20	80	-	-	100
8CS4.2	Image Processing									
8CS4.3	Natural Language Processing									
Total		12	2	-	-	120	480	-	-	400
8CS5	Information System and Securities Lab	-	-	2+2	3	-	-	60	40	100
8CS6	VLSI Design Lab	-	-	2+2	3	-	-	45	30	75
8CS7	X-Windows Programming Lab	-	-	2+2	3	-	-	45	30	75
8CS8	Project Stage II	-	-	2	-	-	-	120	80	200
8CS9	Seminar Presentation	-	-	2	-	-	-	-	-	100
Discipline & Extra Curricular Activities		-	-	-	-	-	-	-	-	50
Total		-	-	16	-	-	-	270	180	600
Grand Total		12	2	16	-	120	480	270	180	1,000