

RAJASTHAN TECHNICAL UNIVERSITY

M.Tech. (VLSI Design) Teaching & Examination Scheme (Full Time)

Semester : I

Course Code	Course	Hrs. / Week			22		
		L	T	P	IA*	Exam	Total
Compulsory Courses							
1MVL1	Advanced Mathematics	3	1		25	100	125
1MVL2	VLSI Design	3	1		25	100	125
1MVL3	Digital System Design	3	1		25	100	125
Elective I							
1MVL4.1	Low Power VLSI Design	3	1		25	100	125
1MVL4.2	Micro Electro Mechanical Systems	3	1		25	100	125
1MVL4.3	Microelectronics	3	1		25	100	125
1MVL5	VLSI Physical Design Lab			3	60	40	100
TOTAL							600

Semester : II

Course Code	Course	Hrs. / Week			Marks		
		L	T	P	IA*	Exam	Total
Compulsory Courses							
2MVL1	Digital Signal Processing	3	1		25	100	125
2MVL2	Analog & Mixed Signal ICs	3	1		25	100	125
2MVL3	CAD of ICs	3	1		25	100	125
Elective II							
2MVL4.1	High Level System Design & Modeling	3	1		25	100	125
2MVL4.2	FPGA Based System Design	3	1		25	100	125
2MVL4.3	Biomedical Electronics	3	1		25	100	125
2MVL5	Digital System Design Lab			3	60	40	100
TOTAL							600

* I.A. – Internal Assessment

M.Tech. (VLSI Design)
Teaching & Examination Scheme (Full Time)

Semester : III

Course Code	Course	Hrs. / Week			Marks		
		L	T	P	IA*	Exam	Total
	Compulsory Course						
3MVL1	Embedded System Design	3	1		25	100	125
	Elective III						
3MVL2.1	VLSI Testing & Testability	3	1		25	100	125
3MVL2.2	Nanoelectronics	3	1		25	100	125
3MVL2.3	Advance IC Technology and Characterization techniques	3	1		25	100	125
3MVL3	Dissertation (Stage-I)				100	-	100
3MVL4	Seminar			3	90	60	150
	TOTAL						500

Semester : IV

Course Code	Courses	Hrs. / Week			Marks		
		L	T	P	IA*	Exam	Total
4MVL1	Dissertation (Stage-II)				-	500	500
	TOTAL						500
GRAND TOTAL							2200

* I.A. – Internal Assessment

